



<b>Form PTO-1449</b> (Modified)		<b>Atty Docket No.:</b> 042390.P9773		<b>Serial No.:</b> 09/738,088			
List of Patents and Publications Statement (Use several sheets if necessary)				<b>Applicant:</b> Wayne A. Wong, et al.			
				<b>Filing Date:</b> December 15, 2000			
<b>REFERENCE DESIGNATION</b>			<b>U.S. PATENT DOCUMENTS</b>				
Examiner Initials		Document No.	Patentee	Class	Sub- Class	Filing date if appropriate	
RGB	AA	4,807,110	James H. Pomerene, et al.	364	<del>200</del>	<del>04/06/1984</del>	
<b>FOREIGN PATENT DOCUMENTS</b>							
No.		Document No.	Date	Country	Class	Sub- Class	Trans- lation
<b>RECEIVED</b> MAR 29 2001							
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)</b> Technology Center 2100							
RGB	AB	Alexander, T., et al. "Distributed Prefetch-buffer/Cache Design For High Performance Memory System," The Second International Symposium on High-Performance Computer Architecture, pages 254-263, February 1996. [Online] Available: <a href="http://citeseer.nj.nec.com/alexander96distributed.html">http://citeseer.nj.nec.com/alexander96distributed.html</a>					
RGB	AC	Charney, M. J., et al., "Prefetching And Memory System Behavior Of The SPEC95 Benchmark Suite," IBM Journal of Research & Development, Vol. 41, No. 3, 25 pages, May 1997. [Online] Available: <a href="http://www.research.ibm.com/journal/rd/413/charney.html">http://www.research.ibm.com/journal/rd/413/charney.html</a>					
RGB	AD	Joseph, D., et al. "Prefetching Using Markov Predictors," The Twenty-fourth Annual International Symposium on Computer Architecture, pages 252-263, June 1997. [Online] Available: <a href="http://citeseer.nj.nec.com/joseph97prefetching.html">http://citeseer.nj.nec.com/joseph97prefetching.html</a>					
Examiner <i>Reginald D. Bragdon</i>			Date Considered <i>10/9/2003</i>				
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